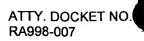
FORM PTO-1449 (Modified)

List of Patents and Publications for **Applicant's Information Disclosure Statement**

(Use several sheets if necessary)



APPLICANT: Allison et al

FILING DATE: Herewith



GROUP:

Reference Designation			US PAT	TENT DOCUMENTS			
Examiner Initials		Document Number	Date	Name	Class/Subclass	Filing Date (If. Appro.)	
82	AA	4,403,303	09/06/83	Howes et al 703/27	384/900	 	
AN AN AND STATE	AB	5,311,509	05/10/94	Heddes et al	370/ 60 - 397		
3/1	AC	5,381,414	01/10/95	Gibson	370/94-9 35-7 211		
8/2	AD	5,396,636	03/07/95	Gallagher et al — 7/3/323	395/750		
V1-	AE	5,404,544	04/04/95	Crayford ——— 113/3/6	3 95/75 0		
1/2_	AF	5,412,782	05/02/95	Hausman et al — 709/250	3 95/25 0		
8/2-	AG	5,446,914	08/29/95	Paul et al —— 769/235	3 95/80 0		
1/1	AH	5,485,584	01/16/96	Hausman et al -710/22	395/842		
25	Al	5,526,355	06/11/96	Vana et al	370/8 5.3 448		
Zes .	AJ	5,560,038	09/24/96	11addack 107/256	3 95/80 0		
8/2	- AK	5,596,575	01/21/97	Yang et al - 3 10/ 468	3 70/468		
2/2	AL	5,604,867	02/18/97	Yang et al — 376/468 Harwood — 709/233 Trainin — 376/230	3 95/200. 13		
1/2	ΑM	5,642,360	06/24/97	Trainin —— 370/230	3 70/230		
Ws.	AN	5,659,758	08/19/97	Gentry et al ——710/260	3 95/73 3		
1	- AO	5,659,794	08/19/97	Caldarale et al 110/260	3 95/824		
fin	_AP	5,689,726	11/18/97	Lin	3 95/830 710/10		
		·	FOREIGN PA	ATENT DOCUMENTS			
		Document				Translation	
		Number	Date	Country	Class/Subclass	Yes No	
Kis	AQ	0752637	01/08/97	EP	G06F 1/26	X	
85-1-	- AR	0777172	06/04/97	EP ·	G06F 1/32	X	
E,	AS	9613106	05/02/96	WO PCT	H04L 12/12	X	
ين الم	– AT	9733408	09/12/97	WO PCT	H04L 12/40	Χ	
XZ	-AU	9715123	Pat App	WO PCT			
	ОТН	ER ART (Inclu	iding Author	, Title, Data, Pertinent Pages	, etc.)	•	
ku	AV ,			Bulletin, Vol. 40, #10, 10/97 "0	Gigabit Speed Multi-l	Protocol Chip and	
- 1		Adapters for	Network Cor	mputing"			
Eu_	- AW	· IBM Technic	al Disclosure	Bulletin, Vol. 38, #08, 08/95 "N	Method of Flow Cont	rol for 100-Mbps	
./		Ethernet Ada					
Ku	- AX	IBM Technic	IBM Technical Disclosure Bulletin, Vol. 37, #04A, 04/94 "Interleaved Dual-Port VRAM for High-				
,	••	Speed Com				-	
JE 2	_AY	IBM Technic		Bulletin, Vol. 38, #07, 07/95 "F	Recovery Method for	Damaged Firmware	
Zu,	ΑZ	IBM Technic	cal Disclosure	Bulletin, Vol. 37, #10, 10/94 "/	Adapter Microcode F	rotection During	
7 2		Download"					
lis	- AW - AX - AY - AZ - AAA	IBM Technic	cal Disclosure	Bulletin, Vol. 38, #12,12/96 "D	efinition of a Global	Wake on Local Are	
, -	-	Makesale For					

Examiner

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Network Frame"

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.